

AC INPUT

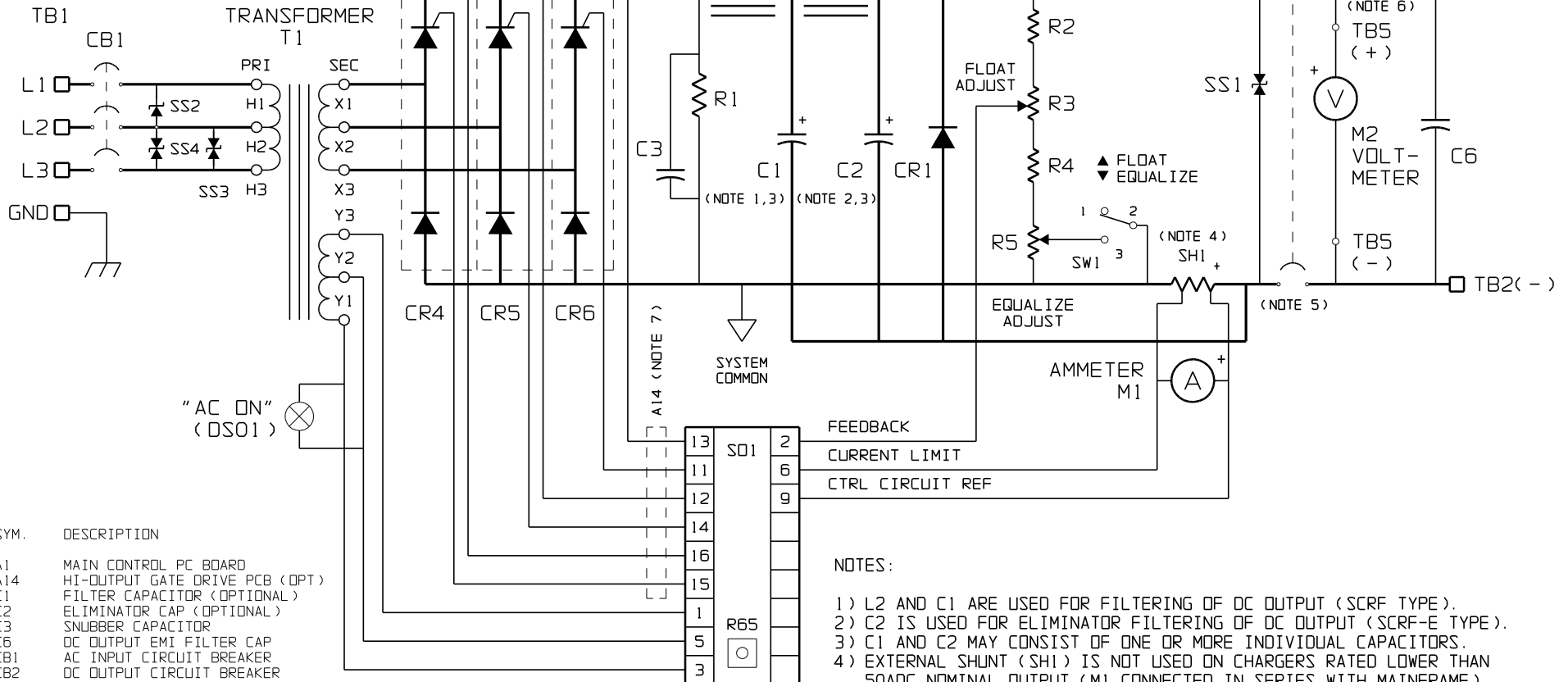
SCR1 SCR2 SCR3

(NOTE 1) CR2

F1

CB2

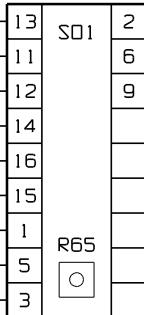
DC OUTPUT



"AC ON"  
(DS01)

| SYM.   | DESCRIPTION                      |
|--------|----------------------------------|
| A1     | MAIN CONTROL PC BOARD            |
| A14    | HI-OUTPUT GATE DRIVE PCB (OPT)   |
| C1     | FILTER CAPACITOR (OPTIONAL)      |
| C2     | ELIMINATOR CAP (OPTIONAL)        |
| C3     | SNUBBER CAPACITOR                |
| C6     | DC OUTPUT EMI FILTER CAP         |
| CB1    | AC INPUT CIRCUIT BREAKER         |
| CB2    | DC OUTPUT CIRCUIT BREAKER        |
| CR1    | POLARITY DIODE                   |
| CR2    | BLOCKING DIODE                   |
| CR4-6  | RECTIFIER DIODES                 |
| DS01   | AC ON INDICATOR LIGHT            |
| F1     | MAIN DC FUSE                     |
| F2     | REDUNDANT DC FUSE (NOT SHOWN)    |
| L1     | MAIN INDUCTOR                    |
| L2     | FILTER INDUCTOR (OPTIONAL)       |
| M1     | DC AMMETER                       |
| M2     | DC VOLTMETER                     |
| R1     | RATING RESISTOR                  |
| R2     | VOLTAGE DIVIDER (TOP)            |
| R3     | FLOAT POTENTIOMETER              |
| R4     | VOLTAGE DIVIDER (LOWER)          |
| R5     | EQUALIZE POTENTIOMETER           |
| R55    | CURRENT LIMIT ADJUST POT         |
| SCR1-3 | SILICON-CONTROLLED RECTIFIERS    |
| SH1    | DC SHUNT                         |
| SD1    | CTRL PCB (A1) INTERCONNECTOR     |
| SS1    | DC OUTPUT SURGE PROTECTOR        |
| SS2-4  | AC INPUT SURGE PROTECTORS        |
| SW1    | FLOAT/EQUALIZE SWITCH (OPTIONAL) |
| T1     | POWER TRANSFORMER                |
| TB1    | AC INPUT TERMINAL BLOCK          |
| TB2    | DC OUTPUT TERMINAL BLOCK         |

CONTROL MODULE  
PC BOARD ASSY. A1  
(GK0048-XX)



NOTES:

- 1) L2 AND C1 ARE USED FOR FILTERING OF DC OUTPUT (SCR-F TYPE).
- 2) C2 IS USED FOR ELIMINATOR FILTERING OF DC OUTPUT (SCR-F-E TYPE).
- 3) C1 AND C2 MAY CONSIST OF ONE OR MORE INDIVIDUAL CAPACITORS.
- 4) EXTERNAL SHUNT (SH1) IS NOT USED ON CHARGERS RATED LOWER THAN 50ADC NOMINAL OUTPUT (M1 CONNECTED IN SERIES WITH MAINFRAME).
- 5) OPTIONAL DC CIRCUIT BREAKER (CB2) IS SHOWN. IF CB2 IS NOT SUPPLIED, A SECOND DC FUSE (F2) IS SUPPLIED IN THE NEGATIVE OUTPUT LEAD.
- 6) TB5 IS USED AS AN INTERNAL CONNECTION POINT FOR BATTERY POSITIVE AND NEGATIVE SIGNAL WIRING ONLY.
- 7) AN AUXILIARY GATE DRIVE MODULE (A14) IS USED ON CHARGERS RATED 175ADC NOMINAL OUTPUT OR GREATER (SEE DRAWING EH0617).

|     |        |        |     |  |  |
|-----|--------|--------|-----|--|--|
|     |        |        |     | THIRD ANGLE PROJECTION   |  |
|     |        |        |     | DRAWN BY MCR 091803  | TITLE SCR/SCR-F SERIES BATTERY CHARGER:<br>MAINFRAME SCHEMATIC - THREE PHASE INPUT |
|     |        |        |     | APPROVED   | DRAWING No JE5078-00 REV 0 A   |
| 0   | E1384  | 091803 |     | UNLESS OTHERWISE NOTED DIMENSIONS ARE IN INCHES. TOLERANCES ARE: |  |
| REV | ECN No | DATE   | APP | SCALE NTS  | PART No JE5078-00 SHEET 1 OF 1   |